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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki  
Serial No. : Unassigned  
Filed : July 31, 2001  
Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD  
THEREFOR

Art Unit : Unknown  
Examiner : Unknown



Commissioner for Patents  
Washington, D.C. 20231

**INFORMATION DISCLOSURE STATEMENT**

Applicant submits the reference listed on the attached form PTO-1449, a copy of which is enclosed.

This statement is being filed with the application. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: July 31, 2001

  
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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 12732-062001	Application No. Unassigned
<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Shunpei Yamazaki	
		Filing Date July 31, 2001	Group Art Unit

 0971 U.S. PTO  
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U.S. Patent Documents							
Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AQ	Shunpei Yamazaki et al.; U.S. Patent Application No. 09/848,233; filed May 4, 2001 (patent pending);
	AR	
	AS	
	AT	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	